

AP/TPW



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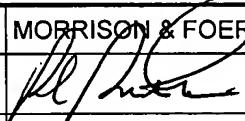
(to be used for all correspondence after initial filing)

		Application Number	10/720,378
		Filing Date	November 25, 2003
		First Named Inventor	Nobuyuki SEKIKAWA
		Art Unit	2814
		Examiner Name	S. Rao
Total Number of Pages in This Submission	9	Attorney Docket Number	492322001810

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input checked="" type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
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<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Request for Refund	<input type="checkbox"/> Return Receipt Postcard
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<input type="checkbox"/> Reply to Missing Parts/ Incomplete Application	<input type="checkbox"/> Remarks	
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	MORRISON & FOERSTER LLP		
Signature			
Printed name	Barry E. Bretschneider		
Date	November 3, 2005	Reg. No.	28,055



PATENT
Docket No. 492322001810

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Nobuyuki SEKIWA *et al.*

Serial No.: 10/720,378

Filing Date: November 25, 2003

For: METHOD OF MANUFACTURING
INSULATED GATE
SEMICONDUCTOR DEVICE

Examiner: Shrinivas H. Rao

Group Art Unit: 2814

Conf. No.: 4282

AMENDMENT UNDER 37 CFR 1.116

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Action mailed August 9, 2005, please amend this application as follows:

The listing of claims begins on page 2.

The Remarks begin on page 5.